

PTO/SB/08A (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/016,939
Sheet	1	of	13	Filing Date	December 14, 2001
				First Named Inventor	Glen E. Roeters
				Art Unit	2829
				Examiner Name	David A. Zarneke
				Attorney Docket Number	DPAC-052A / AK 155574

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
DAZ		US-6,919,626 B2	7/19/2005	Burns	
		US-6,914,324 B2	7/5/2005	Rapport, et al.	
		US-6,908,792 B2	6/21/2005	Bruce, et al.	
		US-6,884,653 B2	4/26/2005	Larson	
		US-6,803,651 B1	10/12/2004	Chiang	
		US-6,709,893 B2	3/23/2004	Moden, et al.	
		US-6,699,730 B2	3/2/2004	Kim, et al.	
		US-6,690,584 B2	2/10/2004	Uzuka, et al.	
		US-6,683,377 B1	1/27/2004	Shim, et al.	
		US-6,677,670 B2	1/13/2004	Kondo	
		US-6,620,651 B2	9/16/2003	He, et al.	
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		US-6,590,282 B1	7/8/2003	Wang, et al.	
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		US-6,552,910 B1	4/22/2003	Moon, et al.	
DAZ		US-6,528,870 B2	3/4/2003	Fukatsu, et al.	

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Examiner Signature	/David Zarneke/	Date Considered	05/18/2006
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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
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Sheet	2	of	13	Attorney Docket Number	DPAC-052A / AK 155574

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DAZ		US-6,514,793 B2	2/4/2003	Isaak	
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DAZ		US-6,281,577 B1	8/28/2001	Oppermann, et al.	
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		US-6,121,676	9/19/2000	Solberg	
		US-6,084,294	7/4/2000	Tomita	
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		US-6,014,316	1/11/2000	Eide	
		US-6,002,167	12/14/1999	Hatano, et al.	
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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 4 of 13

Complete if Known

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DAZ		US-5,917,709	6/29/1999	Johnson, et al.	
		US-5,895,970	4/20/1999	Miyoshi, et al.	
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		US-5,783,464	7/21/1998	Burns	
		US-5,778,552	7/14/1998	Burns	
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		US-5,763,296	6/9/1998	Casati, et al.	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/016,939
				Filing Date	December 14, 2001
				First Named Inventor	Glen E. Roeters
				Art Unit	2829
				Examiner Name	David A. Zarneke
Sheet	8	of	13	Attorney Docket Number	DPAC-052A / AK 155574

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DAZ		US-6,313,998	11/6/2001	Kledzik	
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		US-6,097,087	8/1/2000	Farnworth, et al.	
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DAZ		US-4,722,691	2/2/1988	Gladd, et al.	
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DAZ		US-6,360,433 B1	3/26/2003	Ross	
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DAZ		US-5,523,619	6/4/1996	McAllister, et al.	
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		US-3,766,439	10/16/1973	Isaacson		
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
DAZ		JP 2001/077294 (A)	3/23/2001	Katsumasa		
↓		JP 2001/085592 (A)	3/30/2001	Wada		
		JP 2001/332683 (A)	11/30/2001	Kabasawa		
		JP 2003/088760 (A)	3/20/2003	Masataka, et al.		
↓		JP 2003/086761 (A)	3/20/2003	Iwao, et al.		
DAZ		JP 2003/037246 (A)	2/7/2003	Echigoya		

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Substitute for form 1449/PTO				<div>Complete if Known</div>	
<div>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</div> <div>(Use as many sheets as necessary)</div>				Application Number	10/016,939
				Filing Date	December 14, 2001
				First Named Inventor	Glen E. Roeters
				Art Unit	2829
				Examiner Name	David A. Zameke
				Attorney Docket Number	DPAC-052A / AK 155574
Sheet	2	of	4		

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DAZ		WO 03/037053 A1	5/1/2003	Cady, et al.		
↓		JP 2000/307029 (A)	11/2/2000	Mitsuru		
		JP 2003/309247 (A)	10/31/2003	Mitsuru		
		JP 2003/347475 (A)	12/5/2003	Mitsuru		
		JP 2003/347503 (A)	12/5/2003	Toshiyuki, et al.		✓
DAZ		JP 4-4368167 (A)	12/21/1992	Omura		

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First Named Inventor				Glen E. Roeters	
Art Unit				2829	
Examiner Name				David A. Zameke	
Attorney Docket Number				DPAC-052A / AK 155574	

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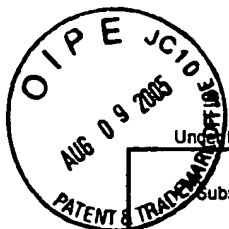
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		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)					
DAZ		JP 4-209562 (A)	7/30/1992	Uzuka, et al.			✓
↓		JP 62-230027 (A)	8/10/1987	Nakamura			
		JP 63-153849 (A)	6/27/1998	Matsubara			
		DE 004214102 A1	12/3/1992	Chun			
		DE 004215467 A1	11/12/1992	Chun			
DAZ		EP 0426-303 (A2)	10/1/1990	Leqq, et al.			

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		Application Number	10/016,939		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Filing Date	December 14, 2001		
		First Named Inventor	Glen E. Roeters		
		Art Unit	2829		
		Examiner Name	David A. Zarneke		
Sheet	1	of	3	Attorney Docket Number	DPAC-052A / AK 155574

NON-PATENT LITERATURE/DOCUMENTS			
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DAZ		Flexible Printed Circuit Technology - A Versatile Interconnection Option. (Website 2 pages) Fjelstad, Joseph. December 3, 2002.	
		Die Products: Ideal IC Packaging for Demanding Applications - Advanced packaging that's no bigger than the die itself brings together high performance and high reliability with small size and low cost. (Website 3 pages with 2 figures) Larry Gilg and Chris Windsor. December 23, 2002. Published on Internet.	
		Tessera uZ Ball Stack Package. 4 figures that purport to be directed to the uZ - Ball Stacked Memory, Published on the Internet.	
		Chip Scale Review Online - An Independent Journal Dedicated to the Advancement of Chip-Scale Electronics. (Website 9 pages) Fjelstad, Joseph, Pacific Consultants L.L.C., Published Jan 2001 on Internet.	
		Flexible Thinking: Examining the Flexible Circuit Tapes. (Website 2 pages) Fjelstad, Joseph., Published 4/20/2000 on Internet.	
		Ron Bauer, Intel. "Stacked-CSP Delivers Flexibility, Reliability, and Space-Saving Capabilities", Volume 3, Spring 2002. Published on the Internet.	
		Tessera Technologies, Inc. - Semiconductor Intellectual Property, Chip Scale Packaging - Website pages (3), Internet	
		Tessera Introduces uZ ä - Ball Stacked Memory Package for Computing and Portable Electronic Products Joyce Smaragdis, Tessera Public Relations, Sandy Skees, MCA PR (www.tessera.com/news_events/press_coverage.cfm); 2 figures that purport to be directed to the uZ ä - Ball Stacked Memory Package. Published 7/17/2002 in San Jose, CA	
		WILLIAM R. NEWBERRY, Design Techniques for Ball Grid Arrays, Xynetix Design Systems, Inc., Portland, Maine, Published on the Internet.	
DAZ		Chip Scale Packaging and Redistribution, Paul A. Magiill, Glenn A. Rinne, J. Daniel Mis, Wayne C. Machon, Joseph W. Baggs, Unitive Electronics Inc.	

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DAZ		Dense-Pac Microsystems, 16 Megabit High Speed CMOS SRAM DPS1MX16MKn3	
		Dense-Pac Microsystems, 256 Megabyte CMOS DRAM DP3ED32MS72RW5	
		Dense-Pac Microsystems, Breaking Space Barriers, 3-D Technology 1993	
		Dense-Pac Microsystems, DPS512X16A3, Ceramic 512K X 16 CMOS SRAM Module.	
		IBM Preliminary 168 Pin SDRAM Registered DIMM Functional Description & Timing Diagrams	
		3D Interconnection for Ultra-Dense Multichip Modules, Christian VAL, Thomson-CSF DCS Computer Division, Thierry Lemoine, Thomson-CSF RCM Radar Countermeasures Division	
		High Density Memory Packaging Technology High Speed Imaging Applications, Dean Frew, Texas Instruments Incorporated	
		Vertically-Integrated Package, Alvin Weinberg, Pacesetter, Inc. and W. Kinzy Jones, Florida International University	
↓		1992 Proceedings, 42nd Electronic Components & Technology Conference, May 18-20, 1992	
DAZ		IBM Technical Disclosure Bulletin, Vol. 23, No. 12, May 1981	

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DAZ		IBM Technical Disclosure Bulletin, Vol. 20, No. 11A, Apr. 1978	
DAZ		IBM Technical Disclosure Bulletin, vol. 32, No. 3B, Aug. 1989	
DAZ		Orthogonal Chip Mount - A 3D Hybrid Wafer Scale Integration Technology, International Electron Device Meeting, IEDM Technical Digest, Washington, D.C., Dec. 6-9, 1987	

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